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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US4

Serial No.: 09/691,004

Filed: October 18, 2000

Due Date: November 18, 2000

Examiner: Unknown

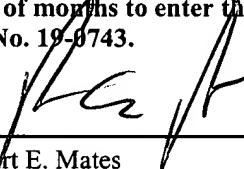
Group Art Unit: Unknown

Commissioner for Patents  
Washington, D.C. 20231

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